ECE 4880 RF Systems Fall 2015 Final Exam Solution

Thermal noise at room temperature: -174dBm + 10log₁₀(BW/1Hz)



I_{1d}	$_{Bcomp} = IIP_{IM3} -$	9.64dB when o	nly IM3 is dominant.
Nonlinear T	aylor coefficie	nts: $A_{IIPIM2} = \left \frac{a}{a} \right $	$\frac{A_1}{2}$; $A_{IIPIM3}^2 = \frac{4}{3} \left \frac{a_1}{a_3} \right $
$\frac{1}{OIP_{IM3,cas}} =$	$=\frac{1}{g_2 \cdot OIP_{IM3,1}}$	$+\frac{1}{OIP_{IM3,2}}$	(IM3 adding coherently)
$\frac{1}{OIP_{IM3,cas}^2} =$	$=\frac{1}{\left(g_2\cdot OIP_{IM3,1}\right)}$	$\frac{1}{)^2} + \frac{1}{OIP_{IM3,2}^2}$	(IM3 adding randomly)
Tł	ne noise factor	of the cascade:	$f_{cas} = f_1 + \sum_{k=2}^{N} \frac{f_k - 1}{\prod_{i=1}^{k-1} g_i};$
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The Leeson empirical approximation for the LO phase noise: $L(\Delta \omega) = \frac{2FkT}{P_{sig}} \cdot \left[1 + \left(\frac{\omega_0}{2Q\Delta\omega}\right)^2\right] \cdot \left(1 + \frac{\Delta\omega_{1/f^3}}{|\Delta\omega|}\right)$

Part I. Multiple Choices (MC) and Fill-ins. There can be more than one correct choices in each MC question. Wrong choices can deduct the points, but you will not get a negative mark. Please write your MC answers clearly to the left blank space to minimize grading errors. **4 pts** each.

1. (Quarter wavelength impedance transform) For a transmission line at quarter wavelength $\lambda/4$ of a given frequency ω_0 with characteristic impedance of Z_0 , the load (complex number) is Z_L and the impedance looking into the transmission line can be represented by Z_l . Z_0 , Z_L and Z_l are all in unit of Ω).



(a) Z_L = Z_l.
(b) Z_L = 1/Z_l.
(c) Z_L·Z_l = Z₀².
(d) If Z_L is inductive (positive imaginary part), then Z_l will be capacitive.
(e) If Z_L is open circuited, then Z_l will contain a nonzero imaginary part.
(f) If Z_L is short circuited, then Z_l = Z₀.

Answer: (c)(d).

Quarter wavelength is half circle in the Smith Chart. Remember that the impedance or admittance is normalized by Z_0 in the Smith Chart. If Z_L is open circuited, then Z_l is short circuited.

2. (Discrete transmission lines) For the discrete-element transmission line shown below, if we match the unit elements $L\Delta z$ and $C\Delta z$ to the distributed line characteristic impedance: $Z_0 = \sqrt{\frac{L}{C}}$ and define

$$v = \frac{1}{\sqrt{LC}} \text{. For an AC signal with } f_0 << f_{bragg},$$

$$c_{Az} \xrightarrow{LAz} \xrightarrow{LAz} \xrightarrow{LAz} \xrightarrow{LAz} \xrightarrow{LAz} \xrightarrow{LAz} \xrightarrow{LAz} \xrightarrow{LAz} f_{bragg} = \frac{1}{\pi\sqrt{L\Delta zC\Delta z}} = \frac{1}{\pi\sqrt{LC}\Delta z} = \frac{v}{\pi\Delta z}$$

- (a) The wavelength λ_0 corresponding to ν/f_0 needs to be much larger than Δz for voltage waveform propagation.
- (b) When we excite a step signal at the left end at t = 0, this signal can appear at the right end instantaneously as there is no loss by resistance.
- (c) We cannot use the Smith Chart to calculate the impedance transformation from load to source as v may not be the speed of light.
- (d) For a broadband AC response, the discrete transmission line will behave like a band-pass filter around f_{bragg} .
- (e) For a broadband AC response, the discrete transmission line will behave like a low-pass filter with the corner frequency around f_{bragg} .

Answer: (a)(e).

The discrete transmission line will behave exactly like a distributed transmission line (or TEM mode in a waveguide) when $f_0 \ll f_{bragg}$. For broadband, it will be like a low-pass filter as higher frequency cannot transmit but only has evanescent mode.

3. (Circulator in a RFID reader) For the RFID transceiver below, the circulator has 1dB pass loss and -60dB isolation. The transmission loss from reader to tag at the given distance is -45dB. The power amplifier (PA) will emit 30dBm. The low-noise amplifier (LNA) has $I_{1dBcomp}$ at -10dBm. $f_{LO} = 1$ GHz.



Block diagrams for RFID. Questions 3 – 8 all used the same parameters and conditions.

- (a) The tag will receive about -30dBm power.
- (b) The LNA will receive about -62dBm power from tags.
- (c) The self jamming will be at the level of -30 dBm power.
- (d) The LNA will have a signal-to-interference ratio of 15dB.
- (e) The LNA will have a signal-to-interference ratio of -5dB.
- (f) We expect significant signal distortion at the receiver path.

Answer: (b)(c).

Tag received power = 30dBm - 1dB - 45dB = -16dBm power. The receiver LNA power from tag = 30dBm - 1dB - 45dB - 45dB - 1dB = -62dBm. Self jamming = 30dBm - 60dB = -30dBm.

SIR = -62dBm - (-30dBm) = -32dB.

Even with the self jamming, the signal now is relatively small at -30dBm away from $I_{1dBcomp} = -10dBm$ by 20dB, so the distortion is expected to be small.

- 4. (PA noise and coherent receiver) The PA above has a gain of 20dB, noise figure of 8dB, $IIP_{IM2} = 25$ dBm, and $IIP_{IM3} = 15$ dBm. The PA output needs an SNR of 20dB for its commands to be correctly decoded by the tag.
 - (a) The SNR at the PA input needs to be around 12dB.
 - (b) The noise power at the PA input needs to be smaller than -18dBm.
 - (c) For thermal noise at -174dBm/Hz, the bandwidth needs to be smaller than 10MHz to satisfy the transmitter SNR requirements.
 - (d) The noise figure of the downconverter mixer is not critical as long as LNA has reasonable gain.
 - (e) As LO is shared between the transmitter and the receiver, the phase noise of LO will not distort the baseband information.
 - (f) As LO is shared between the transmitter and the receiver, the LNA nonlinearity will not cause any DC drift in the direct conversion.

Answer: (b)(d).

 $NF = SNR_{in}/SNR_{out}$, and therefore SNR_{in} needs to be at least 28dB. Through any amplifier, SNR will degrade, not improve. As the input signal and noise will both be amplified, and there are additional noises in the amplifier circuits.

The input signal power to PA needs to be 10dBm. Therefore the noise power needs to be 10dBm - 28dB = -18dBm.

Thermal noise total power at 10MHz bandwidth will be: -174dBm + 70dB = -104dBm, which is far away from -18dBm. Therefore, no bandwidth limitation due to noise here.

Coherent receiver (shared LO for transmitter and receiver; hearing echoes) will not need synchronization between the transmitter and the receiver, but if there is phase noise in LO, it will pollute the signal twice, instead of being cancelled. LNA nonlinearity will cause DC drift, regardless of LO synchronization.

5. (**PA nonlinear parameters**) Use the same PA in question 4 for questions 5 and 6. Fill in the following PA nonlinear parameters, which are independent of the input level. Give both numerical values AND units. Assume nonlinearity is caused by gain saturation and $Z_0 = 50\Omega$.

OIP_{H2}	OIP_{H3}	a_2	a_3

gain = 20dB, $IIP_{IM2} = 25$ dBm and $IIP_{IM3} = 15$ dBm.

$$a_{I} = 10^{20/20} = 10; \ A_{IIPIM2} = \left| \frac{a_{1}}{a_{2}} \right|; A_{IIPIM2} = 5.6 \text{V}. \ |a_{2}| = 1.78 \text{V}^{-1}.$$

 $A_{IIPIM3}^{2} = \frac{4}{3} \left| \frac{a_{1}}{a_{3}} \right|; A_{IIPIM3} = 1.8 \text{V}. \ |a_{3}| = 4.1 \text{V}^{-2}.$

OIP _{H2}	OIP_{H3}	a_2	a_3
51dBm	39.77dBm	$-1.78V^{-1}$	$-4.1V^{-2}$

6. (**PA nonlinear responses**) What would be the correct nonlinear output responses in dBm when the PA output is at 30dBm?

p_{IM2}	p_{H2}	<i>р</i> _{ІМЗ}	p_{H3}

 $p_{in} = 10 dBm; p_{out} = 30 dBm.$

 $p_{IM2} = 30dBm - (25dBm - 10dBm) = 15dBm.$ $p_{H2} = p_{IM2} - 6dB = 9dBm.$ $p_{IM3} = 30dBm - (15dBm - 10dBm) \times 2 = 20dBm.$ $p_{H3} = p_{IM3} - 9.54dB = 10.46dBm.$

p_{IM2}	p_{H2}	<i>р</i> _{ІМЗ}	p_{H3}
15dBm	9dBm	20dBm	10.46dBm

7. (**Phase noise in LO**) You make noise power density measurements for the above LO around 1GHz and obtain the following table.

Δf	10Hz	100Hz	1kHz	10kHz	100kHz	1MHz
Noise power	-73	-104	-124	-145	-165	-164
density (dBm/Hz)						

- (a) Only thermal noise is dominant over the entire range of measured Δf .
- (b) The corner $1/f^3$ frequency is significantly lower than the LO half bandwidth $f_0/2Q$.
- (c) The surface-oriented Flicker noise dominates over thermal noise at $\Delta f = 10$ kHz.
- (d) We can extrapolate the noise power density at 1Hz to be around -63 dBm/Hz.
- (e) We need to consider the DC drift issue for the RFID transceiver.

Answer: (b)(e).

There is clearly $1/f^2$ to $1/f^3$ transition between 10 – 100Hz, so the Flicker corner frequency should be in that range. The extrapolated noise power density at 1Hz would be around -43dBm/Hz. As the noise density at low Δf is significant, there would be DC drift issues in the RFID transceiver.

- 8. (Nonlinearity and noise interplay) The received signal after the downconverter mixer contains many spurious frequency elements (spurs) that make the A/D has high bit error rate. To reduce the number and magnitude of the spurious frequency components,
 - (a) Decrease IIP_{IM3} of the PA.
 - (b) Use a larger LO magnitude to boost the signal level to PA.
 - (c) Reduce the LO phase noise by adding a good-quality bandpass filter.
 - (d) Increase the noise figure of the receiver mixer.
 - (e) Increase the IIP_{H2} of the LNA.
 - (f) Add a bandpass filter between the tag antenna and the tag.

Answer: (c)(e).

Spurs are caused by the interplay of the noise and nonlinearity. We will need to decrease either.

Increase IIP will decrease the nonlinearity. Larger LO can cause the mixer nonlinearity to kick in. Add a good-quality bandpass filter before LO injection will help, but this is often hard as LO is often used to select the frequency. Filters with large tunable range is much harder than tunable LO.

Adding a bandpass filter to the tag antenna will cause minimal effect in reducing spurs, as the signal is already weak at the tag.

- 9. (Use of RF parameter matrices) For the S, T, Y, Z and ABCD matrices in the RF module representation, which choice(s) below are correct? Unilateral means output will not affect input.
 - (a) The matrix representation is for linear, small-signal calculation as a function of frequency.
 - (b) All S and T parameters are dimensionless.
 - (c) ABCD parameters are dimensionless.
 - (d) For unilateral amplifiers, S_{12} and Y_{12} are 0.
 - (e) For unilateral amplifiers, the S and T matrices are not defined in the reverse signal direction.
 - (f) For unilateral amplifiers, the determinant of the ABCD matrix is 0.
 - (g) For unilateral amplifiers, the determinant of the S matrix is always 0.

Answer: (a)(b)(d)(f).

10. (Cascade of RF modules) Two RF amplifiers are cascaded to obtain a larger gain. Amp1 has $g_1 = 10$ dB; $NF_1 = 2$ dB; $OIP_{IM3} = 25$ dBm. Amp2 has $g_2 = 20$ dB; $NF_2 = 10$ dB; $OIP_{IM3} = 15$ dBm. Assume the worst case of IM3 combination in the cascade.

Cascade	$g_{cas}(dB)$	$NF_{cas}(dB)$	<i>OIP_{IM3,cas}</i> (dBm)
$Amp1 \rightarrow Amp2$			
$Amp2 \rightarrow Amp1$			

The worst case in IM3 combination is adding coherently.

Cascade	$g_{cas}(dB)$	$NF_{cas}(dB)$	$OIP_{IM3,cas}(dBm)$
$Amp1 \rightarrow Amp2$	30	3.95	15
$Amp2 \rightarrow Amp1$	30	10	22

Part II. Analysis questions. Please box your final answers clearly. For brief explanation, use 1 - 2 sentences only.

11. (Quadrature hybrid amplifier) For the quadrature hybrid amplifier below with $Z_0 = 50\Omega$, the individual amplifiers can be described by:



a) What are the input and output impedance of Amp1 in Ω ? (2 pts)

$$S_{11} = \Gamma_{Lin} = \frac{V_{-}}{V_{+}} = \frac{Z_{in}/Z_{o} - 1}{Z_{in}/Z_{o} + 1} = 0.1$$
. We can solve to get $Z_{in} = \frac{61\Omega}{2}$. Z_{out} is equal to Z_{in} as $S_{11} = S_{22}$.

b) First assume that Amp1 and Amp2 are identical in every aspect. What are the S parameters for the block in the dash-line box? Remember that S parameters are complex numbers. (4 pts)

As all reflections will be cancelled out at p_{in} and p_{out} due to the -180° phase shift of the two paths. The voltage gain magnitude will remain the same as Amp1 and Amp2 have -3dB input, and then are combined. However, there is a -90° phase shift.

$$\begin{bmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{bmatrix}_{hybrid} = \begin{bmatrix} 0 & 0 \\ -10j & 0 \end{bmatrix}$$

c) The cross capacitance in Amp1 and Amp2 has caused small leakage to have nonzero $S_{12} = -40$ dB. p_{out} is connected to an antenna and leaks in a signal of -20dBm. Will this signal pollute p_{in} ? If so, what is the leakage signal power in dBm at p_{in} ? (4 pts)

The signal from the reverse two paths will add up coherently, both with -90° phase shift, so it will pollute p_{in} .

$$p_{out} = -20 \text{dBm}; \ v_{out} = 0.032 \text{V}.$$

$$v_{pin} = \underbrace{\frac{0.032}{\sqrt{2}} \cdot 0.01 \cdot \frac{-j}{\sqrt{2}}}_{path1} + \underbrace{\frac{-0.032 \, j}{\sqrt{2}} \cdot 0.01 \cdot \frac{1}{\sqrt{2}}}_{path2} = -3.2 \times 10^{-4} \, j$$
Leakage power = $\frac{\left|v_{pin}\right|^2}{2Z_0} = 1.02 \times 10^{-9} W = -60 \text{dBm}.$

d) Now ignore the small effect of S_{12} . If you measure the S parameters for Amp2 and find that they are slightly different as below. Estimate the S parameters for the block in the dash-line box now? Assume the quadrature hybrid is still ideal. (4 pts) For $p_{in} = 0$ dBm, estimate the power dissipated in *Iso_{in}* and *Iso_{out}*. (4 pts)

$$\begin{bmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{bmatrix}_{amp2} = \begin{bmatrix} 0.1 & 0 \\ 8 & 0.15 \end{bmatrix}$$

For S_{11} , it is still matched, so we will have $S_{11} = 0$.

We will need to trace the two different paths for S_{21} and S_{22} .

$$S_{21} = \frac{-j}{\sqrt{2}} \cdot 10 \cdot \frac{1}{\sqrt{2}} + \frac{1}{\sqrt{2}} \cdot 8 \cdot \frac{-j}{\sqrt{2}} = -9j$$

$$S_{22} = \frac{1}{\sqrt{2}} \cdot 0.1 \cdot \frac{1}{\sqrt{2}} + \frac{-j}{\sqrt{2}} \cdot 0.15 \cdot \frac{-j}{\sqrt{2}} = -0.025$$
path1

$$\begin{bmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{bmatrix}_{hybrid} = \begin{bmatrix} 0 & 0 \\ -9j & -0.025 \end{bmatrix}$$

 V_+ at pin will be 0.32V for 0dBm p_{in} .

At *Iso_{in}*, we have the voltage as:
$$V_{-isoin} = \underbrace{0.32 \frac{-j}{\sqrt{2}} \cdot 0.1 \cdot \frac{1}{\sqrt{2}}}_{path1} + \underbrace{0.32 \frac{1}{\sqrt{2}} \cdot 0.1 \cdot \frac{-j}{\sqrt{2}}}_{path2} = -0.032 j$$

The power dissipation at the resistor is then: $p_{isoin} = \frac{(V_{-isoin})^2}{2Z_0} = 10.0 \mu W = -20 \text{dBm}.$

At *Iso*_{out}, we have the voltage as:
$$V_{isoout} = \underbrace{0.32 \frac{-j}{\sqrt{2}} \cdot 10 \cdot \frac{-j}{\sqrt{2}}}_{path1} + \underbrace{0.32 \frac{1}{\sqrt{2}} \cdot 8 \cdot \frac{1}{\sqrt{2}}}_{path2} = -0.32$$

The power dissipation at the resistor is then: $p_{isoout} = \frac{(V_{isoout})^2}{2Z_0} = 1.024mW = 0.10dBm.$

e) If both Amp1 and Amp2 have $IIP_{H2} = 31$ dBm (but their S parameters are slightly different like in part (d)), for $p_{in} = 0$ dBm, estimate the 2nd harmonic H2 power at p_{out} . Assume that H2 will make the total output voltage at Amp1 and Amp2 smaller (i.e., a_2 is negative). (4 pts) Compare your answer of the H2 power if $p_{in} = 0$ dBm is just fed into Amp1. (4 pts)

The S parameters are defined with small signals in the linear system, but we can still calculate the 2^{nd} harmonic voltage in the path separately.

Given $IIP_{H2} = 31$ dBm and $IIP_{IM2} = 25$ dBm, we know $A_{IIPH2} = 5.6$ V and can calculate a_2

 $|a_1| = |a_2|A_{IIPIM 2}$. For Amp1, $a_2 = -1.78$ (V⁻¹) For Amp2, $8 \cdot 11.2 = |a_2|(11.2)^2$; $a_2 = -1.43$ (V⁻¹)

 $v_{in} = 0.32$ V at $p_{in} = 0$ dBm.

$$V_{H2} = \underbrace{-1.78 \cdot \left(0.32 \cdot \frac{-j}{\sqrt{2}}\right)^2 \cdot \frac{1}{\sqrt{2}}}_{path1} + \underbrace{\left(-1.43\right) \left(0.32 \frac{1}{\sqrt{2}}\right)^2 \cdot \frac{-j}{\sqrt{2}}}_{path2} = 0.060 + 0.051j$$

$$p_{outH2} = \frac{\left|0.060 - 0.051j\right|^2}{100} = 60 \mu W = \frac{-12dBm}{-12dBm}.$$

If only Amp1 is used, we know that the linear gain is $20\log_{10}(10) = 20$ dB, and thus,

 $v_{H2} = -1.78 \times 0.32^2 = -0.18$ V. $p_{outH2} = 324 \mu W = -4.9$ dBm.

We can see that p_{outH2} in the quad hybrid is about 6dB lower indeed. The difference comes from the slight mismatch in Amp1 and Amp2. This is mainly from the 3dB lower input power to Amp1 and Amp2 when we use the quadrature hybrid.

For careful students, you may ask why p_{outH2} is off from the other possible calculation of $2 \times p_{outI} - IIP_{H2} = 2 \times 10 \text{dBm} - 31 \text{dBm} = -11 \text{dBm}$? (Notice that dBm is in power, so the previous equation should be read as power²/power = power for the unit consistency). This is exactly 6dBm lower! 6dBm implies that the voltage has been off by 2 times somewhere. The discrepency originates from the definition how H2/IM2 can be related to a_2 ! When we derive the relation of

 $|a_1| = |a_2|A_{IIPIM2}$, two signals of *A* and *B* have been used, which indeed means that the available input power is doubled. For a single signal, actually we should have used $|a_1| = |a_2|A_{IIPH2}$, which makes the two estimates exactly the same.

If you hope to dig further, a complete resolution of the "terminology" is listed in Appendix H of Egan's book.

12. (**Direct-conversion Q-ary transceivers**) A direct-conversion Wi-Fi transceiver with 4-bit per symbol Q-ary modulation is shown below.



a) The receiver signal power P_R can be modeled by $P_R = P_T K \left(\frac{d_0}{d}\right)^{\gamma}$, where P_T is the transmitted

power, *K* and d_0 are empirical constants, and the parameter γ represents the multi-path effect. $\gamma = 2$ for line-of-sight channels, and the typical three-hop indoor path has $\gamma = 6$. By using $P_T = 30$ dBm for both your router and mobile unit, you measure P_R for the line-of-sight and typical indoor three-hop situations. Fill in the table below. (4 pts)

Distance between unit to	0.3m	1m	3m	10m	30m
router					
Receiver signal power at line of sight (dBm)		-20dBm			
Receiver signal power for three-hop indoor (dBm)		-30dBm			

As $10 \times \log_{10} 3 \cong 5$, we can finish the table quickly as:

Distance between unit to	0.3m	1m	3m	10m	30m
router					
Receiver signal power at line of sight (dBm)	–10dBm	-20dBm	<mark>–30dBm</mark>	–40dBm	–50dBm
Receiver signal power for three-hop indoor (dBm)	0dBm	-30dBm	<mark>–60dBm</mark>	<mark>–90dBm</mark>	–120dBm

b) For the baseband channel bandwidth of 20MHz and 36Mb/s bit rate, how would you set the low-pass filter corner frequency interfacing with the data converter? What should be the A/D data converter sampling frequency? Briefly explain. (4 pts)

I would set the low-pass filter corner frequency to 40MHz (tolerance). The sampling frequency needs to be at least 20MHz. As there are 4 bits per symbol, this is more than sufficient for 36Mb/s (error bits, synchronization, pilot overhead, etc.)

c) As the data converter has relatively high frequency, we choose 10 bits for A/D. With no additional variable-gain amplifier, we can only use the LO signal strength to tune the gain in the receiver path. Assume the mixer gain is proportional to the LO voltage magnitude. To accommodate the dynamic range in P_R for the indoor three-hop situation, what is the LO voltage tuning ratio? (4 pts)

We need to accommodate 120dB difference in receiving power, and the range for A/D is at $20\log_{10}2^{10} \approx 60$ dB. That is, LO needs to have a voltage ratio of 60dB, or 1,000. This is rather impractical, and often an explicit VGS will be needed.

d) During the full duplex mode in frequency division, the transmitter and the receiver on the mobile unit are on different channels, which are set up during the initial polling stage. A nearby transmitter of another mobile unit, which can have up to 0dBm leaking to LNA, can however cause received signal desensitization through the LNA nonlinearity. The gain of the LNA is 15dB. Assume the main nonlinearity of LNA is 3^{rd} order. At $P_T = 30$ dBm, estimate the required IIP_{IM3} of the LNA. (**6 pts**) The desensitization amplitude can be estimated by:

Amplitude $(f_a) = a_1 A + \frac{a_3}{4} (3A^3 + 6AB^2) \cong A \left(a_1 + \frac{3a_3}{2}B^2\right)$. Use $Z_0 = 50\Omega$. The signal A

represents the intended channel which is desensitized by B.

To stay away from leakage desensitization, $a_1 > \left| \frac{3a_3}{2} \right| B^2$. The power of the leakage signal is at

0dBm, and the magnitude is B = 0.32 V. This will give $|a_3| < \frac{2a_1}{3B^2} = 105$ (V⁻²).

$$A_{IIPIM3}^2 = \frac{4}{3} \left| \frac{a_1}{a_3} \right| > 0.40; A_{IIPIM3} > 0.63 (V). IIP_{IM3} > 4.0 \text{mW or 6dBm.}$$

- e) For the DC drift concerns, which of the following choice(s) will increase the DC drift at the receiver A/D converter? (**4 pts**)
 - (a) Increasing the LO_R leakage to the antenna.
 - (b) Increasing the LO_T leakage to antenna.
 - (c) Increasing the noise figure of the LO_T mixer.
 - (d) Decreasing IIP_{IM3} of the PA.
 - (e) Decreasing IIP_{IM2} of the LNA.
 - (f) $LO_{R,I}$ and $LO_{R,Q}$ are not exactly differed by 90°.

Answer: (a)(e)(f)

The transmitter is at a different frequency, so LO_T and PA will not affect the DC drift. They can surely affect the bit error rate by noise and nonlinearity interplay along the entire signal chain.

- 13. (**Dual radio receiver**) Assume that your future boss should ask you to add a satellite S-band radio receiver (similar to XM) on top of the existing FM receiver (88 108MHz) with the minimal cost. The satellite radio uses bandwidth 2.320GHz 2.332GHz. The satellite transmitter has agreed to conform to the FM baseband and intermediate frequency at $BW_{bb} = 200$ kHz and $f_{IF} = 10.7$ MHz, respectively.
 - a) How many stations in total can be accommodated in the dual radio? (2 pts)

 $BW_S = 12$ MHz; $BW_{FM} = 20$ MHz. There can be in total 160 stations.

- b) Do we need image rejection filters between the LO and IF mixers for the radio receiver? Briefly explain. (4 pts)
- As BW_{S} , $BW_{FM} < 2f_{IF}$ for both bands, no in-band signal will be the image for the desired signal.
- c) Draw the dual-radio receiver block diagram with an A/D converter of sampling rate at 400kHz. Use an RF switch for toggling between the FM and S bands. Please be as realistic as possible about the bandwidth of modules while minimizing the hardware cost. (6 pts)

The RF part (antenna, band filter, mixer and LO) will need to be separated for S and FM bands. Likely we will need two LO's to avoid the tuning range to be too large.

